



JPC

Docket No.: M4065.0646/P646
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 10/611,892

Confirmation No.: 3670

Filed: July 3, 2003

Art Unit: 2815

For: OPTIMIZED TRANSISTOR FOR IMAGER
DEVICE

Examiner: B. W. Baumeister

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed December 23, 2004, Applicant hereby selects claims 1-52 and 85-93 for continued examination, without traverse. Additionally, Applicant elects the species defined in the Office Action at page 2 as "IA," which is directed to a "4-transistor CMOS active pixel sensor," according to the Office Action. Claims 1-10, 13-29, 31-43, 45-52, and 85-89. At least claims 1, 15, 21, 26, 32, 38, 46, 50, and 85 are generic.

Upon the allowance of a generic claim, Applicant requests allowance of all claims depending from said generic claim as such are necessarily directed to patentable subject matter.

Applicant believes all pending claims are in immediate condition for allowance and respectfully requests examination on the merits and a Notice of Allowance for all pending claims (1-52 and 85-93).

Dated: January 24, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Ryan H. Flax

Registration No.: 48,141

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicant